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1.If a processor does not have any stack pointer register, then

a) It cannot have subroutine call instruction

b) It can have subroutine call instruction, but no nested subroutine calls

c) Nested subroutine calls are possible, but interrupts are not

d) All sequences of subroutine calls and also interrupts are possible

Ans: a

2. Memory access in RISC architecture is limited to instructions

a. CALL and RET

b. PUSH and POP

c. STA and LDA

d. MOV and JMP

Ans: c

3. A stack organised computer uses instruction of

a) indirect addressing

b) two addressing

c) zero addressing

d) index addressing

Ans: c

4. If we record any music in any recorder, such types of process is called

a) Multiplexing  
b) Encoding  
c) Decoding  
d) None of the Mentioned

Ans: b

5. How many 8 k × 1 RAMs are required to achieve a memory with a word capacity of 8 k and a word length of eight bits?

Eight  
Two  
One  
Four

Ans: a

6. The PROM starts out with

a) 1s  
b) 0s  
c) Null  
d) Both 1s and 0s

Ans: b

7. How is an encoder different from a decoder?

a) The output of an encoder is a binary code for 1-of-N input  
b) The output of a decoder is a binary code for 1-of-N input  
c) The output of an encoder is a binary code for N-of-1 output  
d) The output of an decoder is a binary code for N-of-1 output

Ans: a

8.Auto reload mode is allowed in which mode of the timer?  
a) Mode 0  
b) Mode 1  
c) Mode 2  
d) Mode 3

Ans: c

9.The address space in ARM is \_\_\_\_\_\_  
 224  
 264  
216  
232

Ans: d

10.What are the contents of the IE register, when the interrupt of the memory location 0x00 is caused?  
a) 0xFFH  
b) 0x00H  
c) 0x10H  
d) 0xF0H

Ans: b